# UNITED STATES PATENT AND TRADEMARK OFFICE

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
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| 10/600,245 | 06/20/2003 | Sandeep Bhatia | 14532US01 | 5543 |

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| EXAMINER |
|---|
| VO, TUNG T |

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| 2621 | |

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**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

PTOL-90A (Rev. 04/07)

-- *The MAILING DATE of this communication appears on the cover sheet with the correspondence address* --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE <u>03</u> MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.
- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
  Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

1)☒ Responsive to communication(s) filed on <u>19 March 2007</u>.

2a)☐ This action is **FINAL**.　　2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

4)☒ Claim(s) <u>1-11,15 and 16</u> is/are pending in the application.

　　4a) Of the above claim(s) <u>12-14</u> is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) <u>1-11,15 and 16</u> is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

**Application Papers**

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on <u>26 June 2006</u> is/are: a)☒ accepted or b)☐ objected to by the Examiner.

　　Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

　　Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

　　a)☐ All　b)☐ Some * c)☐ None of:

　　　1.☐ Certified copies of the priority documents have been received.

　　　2.☐ Certified copies of the priority documents have been received in Application No. _____.

　　　3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

　　* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

1)☒ Notice of References Cited (PTO-892)

2)☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3)☐ Information Disclosure Statement(s) (PTO/SB/08)
　　Paper No(s)/Mail Date _____.

4)☐ Interview Summary (PTO-413)
　　Paper No(s)/Mail Date _____.

5)☐ Notice of Informal Patent Application

6)☐ Other: _____.

## DETAILED ACTION

### *Claim Rejections - 35 USC § 103*

1.      The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all

obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in
> section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are
> such that the subject matter as a whole would have been obvious at the time the invention was made to a person
> having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the
> manner in which the invention was made.

1.      Claims 1-11, and 15-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over

Kono et al. (US 2001/0005398) in view of Miyawaki et al. (US 6,408,100).

Re claims 1, 5, 8-11, 15, and 16, Kono discloses a circuit (fig. 6) for displaying images

on a display, said circuit comprising: a first processor (52 of fig. 6) is a decoder; a first memory

(54 of fig. 6, Note the decode control section holds the initial decode starting instruction and

slice layer decode starting instruction, which is considered as a memory) operably coupled to the

first processor, said first memory storing a plurality of instructions for execution by the first

processor, wherein the plurality of executable instructions cause: decoding (63 of fig. 6) encoded

images and parameters associated with the images, thereby resulting in decoded images, and

decoded parameters (PATH FOR WRITING/READING DECODED PICTURE AND

PARAMATERS, 62 of fig. 6, Note writing is storing the decoded picture and parameters into the

display control section, 55 of fig. 6; so the display control section (55 of fig. 6) would obviously

has a memory for storing the decoded picture and parameters) associated with the decoded

images (71 of fig. 6); storing the decoded images (53a-53b of fig. 6) and parameters associated

with decoded picture in the parameter storing area (53e of fig. 6, see also 53f, 53g, and 53h of

fig. 6); a second processor (55 of fig. 6) operably coupled to the status register (57 of fig. 6) for

updating an image to be display on a display unit ([0075], [0084]-[0085]); a second memory (57

and 53f-53h of fig. 6) operably coupled to the second processor, said second memory storing a

plurality of instructions (a display starting instruction, 68 of fig. 6) for execution by the second

processor (55 of fig. 6), wherein the plurality of executable instructions cause: presenting the

images indicated by the display starting instruction for display (68, 74 of fig. 6; S16 of fig. 8);

wherein the instructions causing presenting the images further comprise instructions causing

receiving the decoded parameters and displaying the decoded images based on the decoded

parameters (DISPLAY UNIT of fig. 6; [0075], [0085]).

It is noted that Kono does not particularly teach a queue comprises a FIFO for storing

indicators indicating images to be displayed, and wherein the queue stores the indicators in a

particular order, and wherein the display engine displays the picture associated with the

indicators in an order corresponding to the order that the indicators are stored in the queue as

claimed.

However, Miyawaki teaches a queue (20 of fig. 1) comprises a FIFO (20 of fig. 2) for

storing indicators (display parameters are stored separately) indicating images to be displayed

(reads the stored display parameters in the order of the display pictures), and wherein the queue

stores the indicators in a particular order (col. 1, lines 64-65; Note reads the stored display

parameters in the order of the display pictures), and wherein the display engine (19 of fig. 1)

displays the picture associated with the indicators in an order corresponding to the order that the

indicators are stored in the queue (col. 1, line 65-col. 2, line 10).

Therefore, taking the teachings of Kono and Miyawaki as a whole, it would have been obvious to one of ordinary skill in the art to incorporate the teachings of Miyawaki into the circuit of Kono to reduce the configuration of a display parameter synchronous holding circuit and to simplify its control operation as suggested by Miyawaki (col. 1, lines 55-59).

Re claims 2 and 6, Kono further discloses parameter buffers (53f-53h of fig. 6) for storing the decoded parameters associated with the images.

Re claims 3 and 7, Kono further discloses wherein the display engine (55 of fig. 6) presents the images indicated by the queue for display by receiving the decoded parameters and displaying the decoded images based on the decoded parameters (DISPLAY UNIT of fig. 6).

Re claim 4, Kono further discloses wherein the decoder comprises a first processor (54 of fig. 6) and the display engine comprises a second processor (DISPLAY UNIT of fig. 6).

## *Conclusion*

2.      The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Koda (US 6,546,189) discloses method and apparatus for editing compressed moving pictures and storage medium.
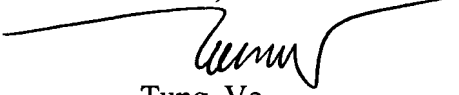
## *Contact Information*

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tung Vo whose telephone number is 571-272-7340. The examiner can normally be reached on Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mehrdad Dastouri can be reached on 571-272-7418. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Tung Vo
Primary Examiner
Art Unit 2621